CMPE212 Lab9 Counters

Objective
In class and discussion, we looked at different implementations of binary counters. A simple way to create a counter is to treat each bit as its own element requiring memory and combine them with some logic.

Procedure
Use your D Flip Flop that you developed last week and create a counter that will count up to 16. You may use the original D Flip Flop, or create a T Flip Flop or JK Flip Flop, to do this. You must use flip flops in your design and your testbench must NOT perform any of the functionality of the counter.

Submission
Just a reminder, if you do not finish the labs, the lab then becomes homework to be done and submitted individually. They will be due on the day of the next lab. So if you did not finish your D Flip Flop last week, then you must submit your verilog code today. The command for doing so is submit cmpe212 lab8 files. I want you to submit your module code, testbench, and makefile.